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- 13 - 2.9 When the parallel load input = 1, the clock pulses go through the AND gate and the data inputs are loaded into the register when the parallel load input = 0, the output of

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computer activities Computer Organisation and Architecture " p.8/ Clock and Clock Speed A very fast clock times and regulates the CPU ... Computer Organisation and Architecture " p.34/ Process Management Every program is a process (see the Task Manager on a Windows computer)

Computer Organisation and Architecture

1.1 Computer architecture. refers to those attributes of a system visible to a programmer or, put another way, those attributes that have a direct impact on the logical execution of a program. Computer organization. refers to the operational units and their interconnections that realize the architectural specifications.

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Microarchitecture - Wikipedia

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